IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph on page 3, between lines 17-22 of the specification with the following:

One problem with voltage-programmed pixels, particularly using polysilicon thin film transistors, is that different transistor characteristics across the substrate (particularly the threshold voltage) give rise to different relationships between the gate voltage and the source-drain current, and ~~artefacts~~ artifacts in the displayed image result. Particularly at low brightness levels, these displays suffer non-uniformity.

Replace the paragraph on page 10, between lines 11-16 of the specification with the following:

The high level of the address voltage needs to be higher than the supply voltage, V_{SUPPLY}, when driving the display element, in order to ensure the address transistor 32 remains off (in the reverse as well as forward direction) regardless of the voltage on

the gate of the drive transistor 22. As shown in FIG. 4, the high address line voltage can be set at the supply voltage V_{SUPPLY} plus the maximum shift voltage 46.